

**TRAY FOR STORING AND TRANSPORTING SEMI-CONDUCTOR AND OTHER  
MICROELECTRONIC COMPONENTS**

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**FIELD OF THE INVENTION**

The present invention relates to a tray for storing and transporting miniature electronic components, commonly known as a chip trays.

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**BACKGROUND OF THE INVENTION**

Microelectronic components go through many processing steps during their manufacturer. Microelectronic components have been getting smaller and smaller as time goes by. Commonly, in fabricating facilities microelectronic components such as chips and other components are transported in trays. Generally the trays have numerous form fitting compartments so that numerous microelectronic components can be handled and transported at one time. Generally, so called chip trays are designed to be stackable so that multiple trays can be transported. Thus one chip tray forms the cover for the chip tray immediately below it. When a desired number of trays are stacked for transport a dedicated cover is applied to the top tray to keep the components covered and secure.

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As microelectronic components have become smaller they have become more difficult to handle. In particular some microelectronic components have become so small that their overall size is less than the tolerances that can reasonably be attained in the manufacturing of chip trays. Thus it has become prohibitively difficult and expensive to manufacture form fitting chip trays with tight fitting lids that are sufficiently close to the 20 microelectronic components to keep them from shifting while the chip tray is in transit.

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In the past, several materials have been interposed between chip trays in an effort to keep components in place. These materials include rice paper, Tyvek® and various

films about three to seven mils thick. These components have mixed success in the retention of very small components.

Thus it would be a benefit to the microelectronics industry to have access to a chip tray with a lid that would provide for excellent retention of extremely small 5 microelectronic components while at the same time preventing those microelectronic components from being damaged.

#### SUMMARY OF THE INVENTION

The present invention is a chip tray that solves many of the above-mentioned 10 problems. The chip tray of the present invention includes on its underside a resiliently compressible sheet member that resiliently retains microelectronic components in the chip tray below by pressing gently but firmly down on the microelectronic components and the separators between cavities that contain the components. The surface of compressible sheet member that confronts the top of the chip tray and component either extends slightly into the 15 pocket to secure the component or is indented to conform to the component contour that extends above the top surface of the tray.

The chip tray of the present invention generally includes a central plate having an upper surface and a lower surface and a supporting perimeter structure or legs. The invention may be utilized with stackable chip trays or chip tray lids of any configuration. 20 The upper surface of the plate generally includes a plurality of indented form fitting cavities into which microelectronic components may fit. Each cavity is molded appropriately to support and contain a specific microelectronic component. The lower surface of the chip tray or cover of the present invention is covered by a resilient sheet layer. The resilient sheet layer may be formed, for example, by open or close cell resilient foam. The resilient sheet 25 like member may include memory foam, an elastomeric polymer, polyethylene foam or a

static dissipative cross-linked polyethylene foam. Some foams appropriate for this use are described in United States Patent Application Serial No 09/971,352 and United States Patent No. 6,286,684B1 both of which are incorporated herein in their entirety by this reference.

The resilient sheet layer is desirably held to the lower surface of the chip tray  
5 by an adhesive but may be mechanically secured to the lid or unsecured and separate from the tray or lid. If the resilient sheet layer is adhesively secure to the chip tray, the adhesive may be selected from contact adhesives, low ionic double-faced tape, or a heat sensitive adhesive such as a hot melt adhesive may be used as well.

The resilient sheet is selected so that it will be compressed slightly when  
10 placed on top of a chip tray filled with microelectronic components thus providing a gentle but firm holding force to keep the microelectronic components in their desired cavities. Desirably the resilient sheet layer deflects by at least about ten percent of its thickness when applied to the chip tray component cavities. For example, for a tray that has a space of 0.08 inch between adjacent trays the resilient layer may be chosen to be about 0.10 inch thick  
15 allowing for a compression of 0.02 inch. A desirable range of compression is from five to twenty five percent of the thickness of the resilient layer or from 0.01 to 0.05 inch.

An advantage and feature of particular embodiments is that a chip tray is provided that is capable of securing different size components, particularly components with different heights in the pockets of the chip tray.

20 A further advantage and feature of the invention is that the components are gently secured in the pockets of the chip tray and sliding and rattling of the components in the pocket is minimized or eliminated.

A further advantage and feature of the invention is that the foam material can be static dissipative to minimize potential for damage from static electricity.

BRIEF DESCRIPTION OF THE DRAWINGS

Fig. 1 is a top plan view of a chip tray in accordance with the present invention.

Fig. 2 is a cross sectional view taken along section line 2-2 of Fig. 1.

5 Fig. 3 is a detailed cross sectional view taken from detail A of Fig.2.

Fig. 4 is a cross sectional view of two stacked chip trays depicting a resilient layer retaining a microelectronic component.

Fig. 5 is a detailed cross sectional view of two stacked chip trays depicting a resilient layer retaining a microelectronic component and resiliently extending into a cavity 10 containing the microelectronic component.

Fig. 6 is a cross sectional view of two stacked chip trays and a chip tray cover depicting two resilient layers retaining microelectronic components.

Fig. 7 is a cross sectional view of a chip tray and cover in accordance with an embodiment of the invention.

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DETAILED DESCRIPTION OF THE INVENTION

The microelectronics transport tray 10 of the present invention as depicted in Figs. 1-3 generally includes a body portion or plate 12, a rail or plate surround 14 and a resiliently compressible sheet member 16.

20 Plate 12 generally includes a top surface 18 and a bottom surface 20. Plate 12 is depicted herein as a square shape but plate 12 may take any shape desired. Top surface 18 will generally include a plurality or multiplicity of cavities 19 which are shaped and sized to accept a particular microelectronic component and that have a matrixical arrangement on the top surface. The microelectronics transport tray 10 as described in this application is 25 exemplary. The present invention may be utilized with other stackable microelectronics

transport tray 10 or lid 21. The use of the invention with a lid will be readily apparent to those skilled in the art.

Plate 12 is bound on its perimeter by plate support 14. Plate surround 14 generally supports plate 12 in such a way that microelectronics transport tray 10 is stackable 5 upon a similar tray 10. In this embodiment of the invention plate surround includes leg 22. Leg 22 is configured so as to extend downward and outward from plate 12 in a stair step fashion. Thus in this embodiment of the invention leg 22 defines, on its upper surface, first indentation 24 and second indentation 26. Desirably plate 12 also defines rounded corner 28. While the embodiment disclosed here is supported by plate surround 14, it is to be understood 10 that the present invention can make use of any sort of support member including legs, pegs or protrusions to support microelectronics transport tray 10 in a stackable fashion upon similar microelectronics transport tray 10.

Bottom surface 20 of tray 10 defines flat recess 30 and indented perimeter 32. Flat recess 30 desirably covers the bottom of plate 12 entirely. Resilient sheet 16 is desirably 15 received into flat recess 30. Resilient sheet 16 covers substantially all of flat recess 30. Resilient sheet 16 has a thickness slightly greater than the depth of flat recess 30 so that resilient sheet 16 when received in flat recess 30 extends beyond the depth of flat recess 30 slightly.

Resilient sheet 16 is desirably formed of an open or closed cell cell resilient 20 polymer foam. However, resilient sheet 16 may be formed of any resilient material that will provide a yielding resilient surface appropriate to apply a small force to microelectronic components transported in tray 10. The resilient sheet member may include memory foam, an elastomeric polymer, polyethylene foam or static dissipative cross-linked polyethylene foam.

Resilient sheet 16 may be secured to flat recess 30 by adhesive or mechanical 25 means. For example, resilient sheet 16 may be secured by a contact adhesives, low ionic

double-faced tape, or a heat sensitive adhesive such as a hot melt adhesive may be used as well. In some applications it may be contagious to secure resilient sheet 16 to tray 10 with a hot melt adhesive so that it is necessary and desired for cleaning resilient sheet 16 can be released from flat recess 30 by appropriate heating.

5 Referring to Figure 4, two microelectronics transport trays 10 are shown in a stacked orientation with a microelectronic component 34 therebetween. Microelectronic component 34 rests in form fitting cavity 36 and is held in place by resilient sheet 16 pressing down there upon.

Referring to Fig 5, a membrane 38 may be interposed between resilient sheet 10 16 and microelectronic component 34. Membrane 38 may be bonded to resilient sheet 16 or be interposed as a separate layer. Note that resilient sheet 16 deforms and extends into cavity 36 to a small degree. This deformation helps to assure that microelectronic component 34 is securely and safely held within cavity 36 thus preventing potential damage to microelectronic component 34 while in transit.

15 Referring to Fig. 6, two microelectronics transport trays 10 are shown in a stacked orientation with a microelectronic component 34 therebetween. In addition, tray lid 21 covers the upper of the two microelectronics transport trays 10. Both microelectronics transport trays 10 and tray lid 21 are provided with resilient sheet 16 to protect microelectronic components 34 contained therebetween.

20 Referring to Fig. 7, is a cross sectional view of a chip tray 10 and cover 21 with the component 34 having a thickness greater than the pocket height and the resiliently compressible sheet member downward surface 51 deflected to absorb the contour of the component.